

# SysML V2: How the New Language Will Address Systems Engineering & Digital Thread Challenges

with the participation of



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**Sébastien Dubé**  
LGM



**Ibrahim Saddoug**  
MathWorks



**Laurent Royer**  
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**Etienne Juliot**  
OBEO



**Thomas Marchand**  
Dassault Systèmes



**Marc-Antoine Azar**  
Dassault Systèmes

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**Raphaël Faudou**  
SAMARES



**Thierry Le Sergent**  
ANSYS part of Synopsys



**Rachna Harsh**  
PTC



**Corinne Bacle**  
IBM



**Serge Bonnaud**  
IBM



**Antoine Zuin**  
CIL4Sys

# Program

## Thematic Day SysML V2

May 28, 2026

- 08:30 — Welcome Coffee
- 09:00 — Introduction | Katia Potiron & Luca Palladino ♦ **AFIS**  
Welcome word ♦ **INSA Centre Val de Loire**
- 09:10 — SysML v2 | The Origin of the Language | Sébastien Dubé ♦ **LGM**
- 09:40 — Digital Thread Example | Etienne Juliot ♦ **OBEO**
- 10:20 — Coffee Break
- 10:50 — Digital Thread Example | Ibrahim Saddoug & Laurent Royer ♦ **MathWorks**
- 11:30 — Digital Thread Example | Thomas Marchand & Marc-Antoine Azar ♦ **Dassault Systèmes**
- 12:10 — Lunch
- 13:10 — First Industrial Feedback | Raphaël Faudou ♦ **SAMARES**
- 13:40 — Digital Thread Example | Thierry Le Sergent ♦ **ANSYS**
- 14:20 — Digital Thread Example | Rachna Harsh ♦ **PTC**
- 15:00 — Coffee Break
- 15:20 — Digital Thread Example | Corinne Bacle & Serge Bonnaud ♦ **IBM**
- 16:00 — Usage de SysML V2 | Antoine Zuin ♦ **CIL4Sys**
- 16:30 — Conclusion Luca Palladino ♦ **AFIS/SAFRAN**

